

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Kenneth S. McElvain

Examiner: Clark, Sheila V

Appl. No. : 10/626,031

Art Unit: 2823

Filed : July 23, 2003

Confirmation No. 1272

For : Integrated Circuit Devices and  
Methods and Apparatuses for  
Designing Integrated Circuit  
Devices**CERTIFICATE OF TRANSMISSION**  
I hereby certify that this correspondence is  
being submitted electronically via EFS Web on  
the date shown below.

Customer No. : 08791

/Judith Szepesi/ June 14, 2010  
**Judith A. Szepesi** **Date**Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**STATUS INQUIRY**

Sir:

The above-captioned application was filed more than 18 months ago and no substantive Office Action has issued to date. Furthermore, no First Action Prediction is available via PAIR.

Please provide the current status of the application and an indication of when the first action is expected to issue.

Respectfully submitted,  
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: June 14, 2010

/Judith Szepesi/  
Judith A. Szepesi  
Reg. No. 39,393Customer No. 08791  
1279 Oakmead Parkway  
Sunnyvale, CA 94085  
(408) 720-8300